# Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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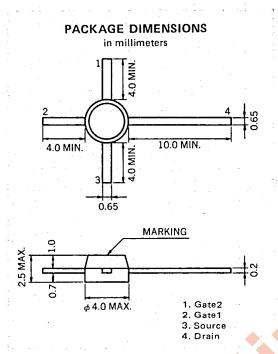
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# MES FIELD EFFECT TRANSISTOR 3SK203

# RF AMP. FOR UHF TV TUNER N-CHANNEL GaAs DUAL GATE MES FIELD-EFFECT TRANSISTOR 4PIN MINI MOLD



### **FEATURES**

Suitable for use as RF amplifier in UHF TV tuner.

Low C<sub>rss</sub>: 0.02 pF TYP.
 High G<sub>PS</sub>: 20 dB TYP.
 Low NF: 1.1 dB TYP.

## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Drain to Source Voltage	V <sub>DSX</sub>	10	V
Gate1 to Source Voltage	$V_{G1S}$	<b>-4.5</b>	٧
Gate2 to Source Voltage	$V_{G2S}$	-4.5	٧
Drain Current	$I_D$	60	mΑ
Total Power Dissipation	PT	200	mW
Channel Temperature	T <sub>ch</sub>	125	°C
Storage Temperature	$T_{stg}$	-55 to +125	°C

## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

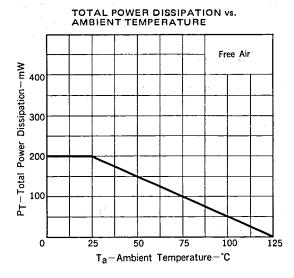
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Drain to Source Breakdown Voltage	BVDSX	10			V	$V_{G1S} = -4 V$ , $V_{G2S} = 0$ , $I_D = 20 \mu A$	
Drain Current	IDSS	8.0		60	mA	V <sub>DS</sub> = 5 V, V <sub>G1S</sub> = 0, V <sub>G2S</sub> = 0	
Gate1 to Source Cutoff Voltage	VG1S(off)			-3.5	V	V <sub>DS</sub> = 5 V, V <sub>G2S</sub> = 0, 1 <sub>D</sub> = 100 μA	
Gate2 to Source Cutoff Voltage	VG2S(off)			-3.5	V	V <sub>DS</sub> = 5 V, V <sub>G1S</sub> = 0, I <sub>D</sub> = 100 μA	
Gate1 Reverse Current	I <sub>G1SS</sub>			10	μΑ	V <sub>DS</sub> = 0, V <sub>G1S</sub> = -4 V, V <sub>G2S</sub> = 0	
Gate2 Reverse Current	IG2SS			10	μА	V <sub>DS</sub> = 0, V <sub>G2S</sub> = -4 V, V <sub>G1S</sub> = 0	
Forward Transfer Admittance	lyfsl	22	30		mS	V <sub>DS</sub> = 5 V, V <sub>G2S</sub> = 1 V, I <sub>D</sub> = 10 mA, f = 1.0 kHz	
Input Capacitance	Ciss	0.7	1.2	1.7	pF	V <sub>DS</sub> = 5 V, V <sub>G2S</sub> = 1 V, I <sub>D</sub> = 10 mA, f = 1.0 MHz	
Reverse Transfer Capacitance	Crss		0.02	0.035	рF		
Power Gain	GPS	16.0	20.0		dB	V <sub>DS</sub> = 5 V, V <sub>G2S</sub> = 1 V, I <sub>D</sub> = 10 mA, f = 900 MHz	
Noise Figure	NF		1.1	2.5	dB		

#### IDSS Classification (Unit: mA)

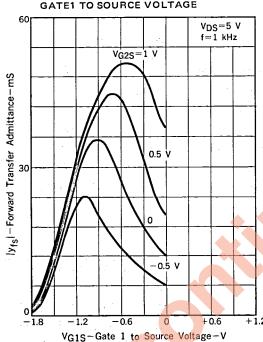
Marking	N	М	L	K
IDSS	8 to 25	20 to 35	30 to 50	45 to 60

PRECAUTION: Avoid high static voltages or electric fields so that this device would not suffer from any damage due to those voltage or fields.

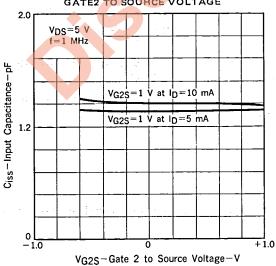
## TYPICAL CHARACTERISTICS (Ta = 25°C)



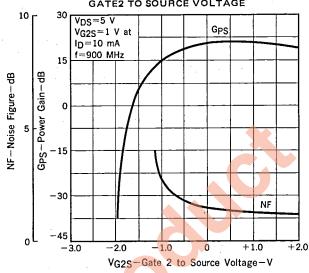
# FORWARD TRANSFER ADMITTANCE vs. GATE1 TO SOURCE VOLTAGE



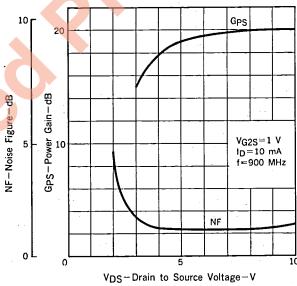
# INPUT CAPACITANCE vs. GATE2 TO SOURCE VOLTAGE

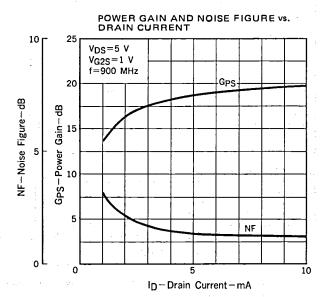


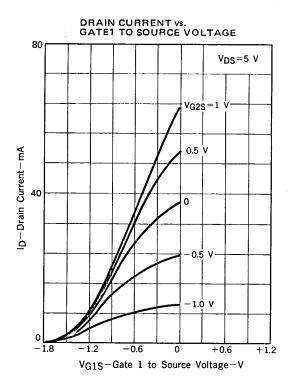
#### POWER GAIN AND NOISE FIGURE vs. GATE2 TO SOURCE VOLTAGE

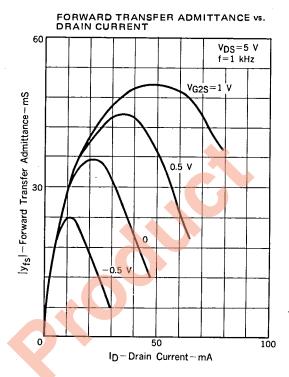


# POWER GAIN AND NOISE FIGURE vs. DRAIN TO SOURCE VOLTAGE









## S-PARAMETER ( $V_{DS} = 5 \text{ V}, V_{G2S} = 1 \text{ V}, I_D = 10 \text{ mA}$ )

ANG
ANG
-1.5
-4.2
-5.1
-8.3
-8.6
-12.9
-12.6
-16.5
-17.5
-21.4
-22.6
-25.4

## 900 MHz $G_{\mbox{\footnotesize{PS}}}$ and NF Test circuit

