

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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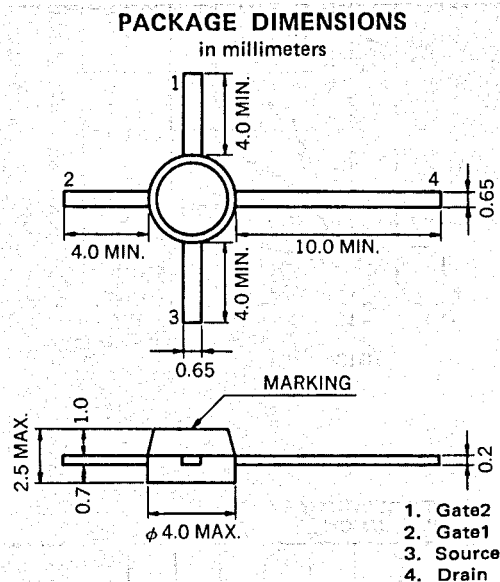
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RF AMP.FOR UHF BAND

N-CHANNEL GaAs DUAL GATE MES FIELD-EFFECT TRANSISTOR

4PIN DISK MOLD

**FEATURES**

- Suitable for use as RF amplifier through UHF band.
- Low C_{rss} : 0.02 pF TYP.
- High G_{PS} : 20 dB TYP.
- Low NF: 1.1 dB TYP.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

Drain to Source Voltage	V_{DSX}	10	V
Gate1 to Source Voltage	V_{G1S}	-4.5	V
Gate2 to Source Voltage	V_{G2S}	-4.5	V
Drain Current	I_D	80	mA
Total Power Dissipation	P_T	200	mW
Channel Temperature	T_{ch}	125	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +125	$^\circ\text{C}$

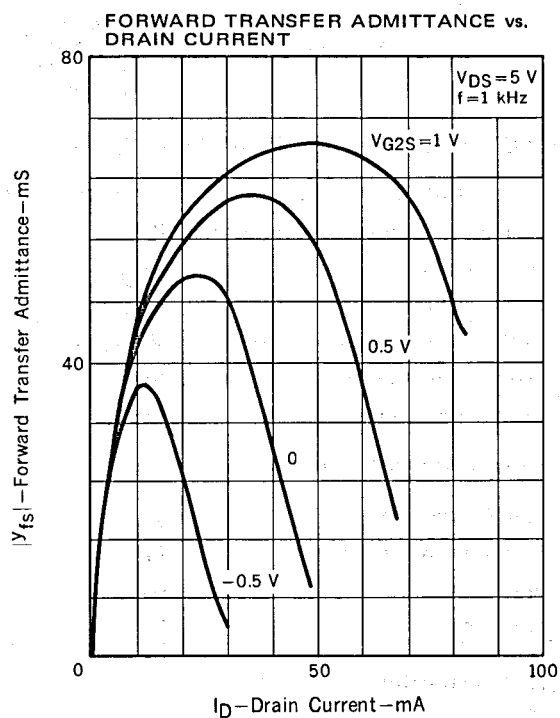
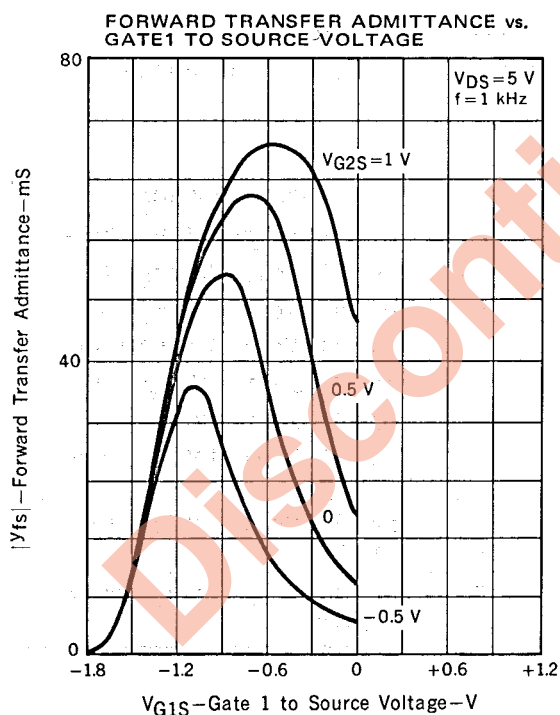
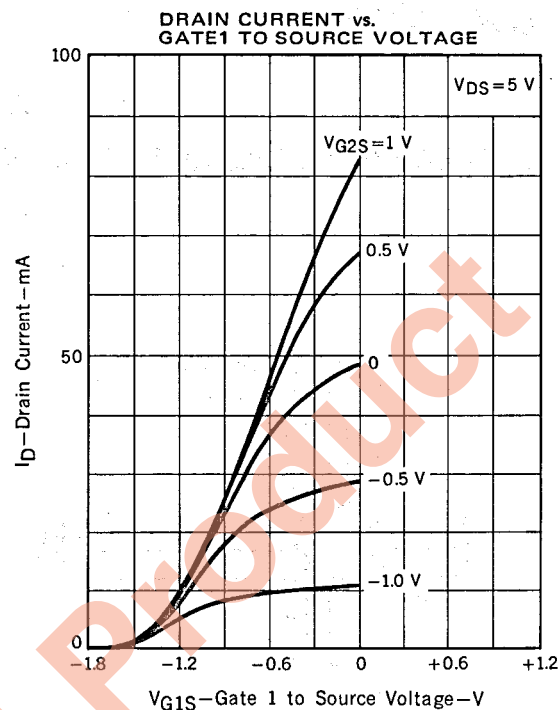
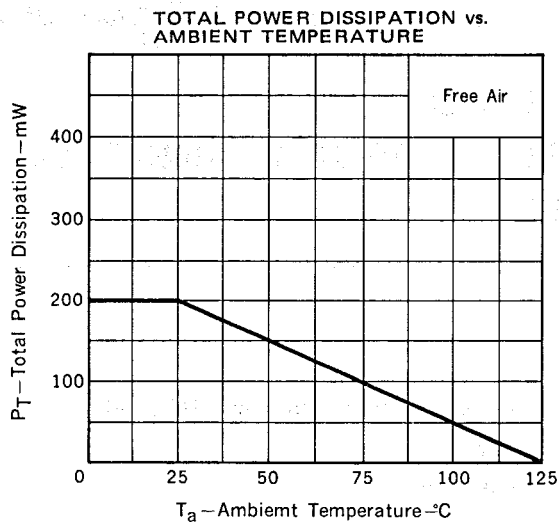
ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

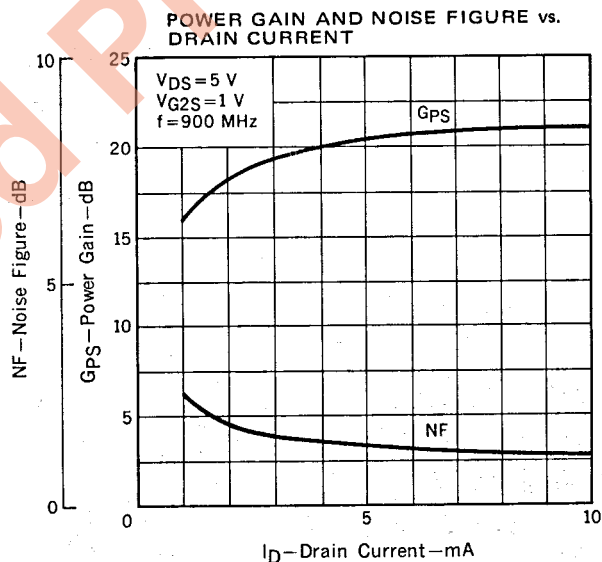
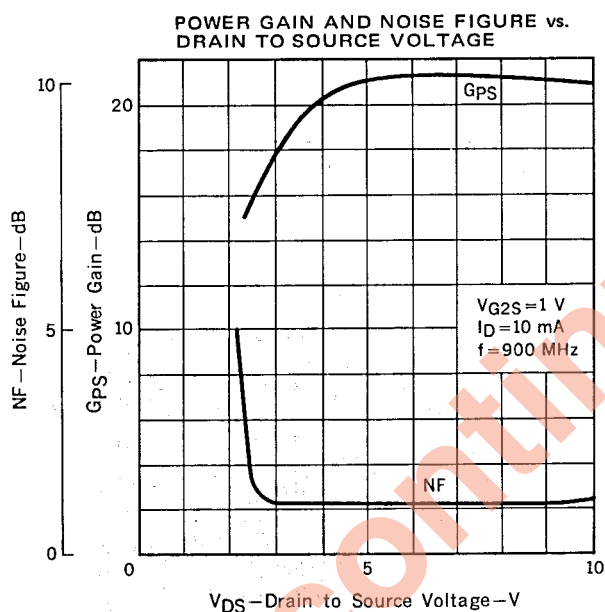
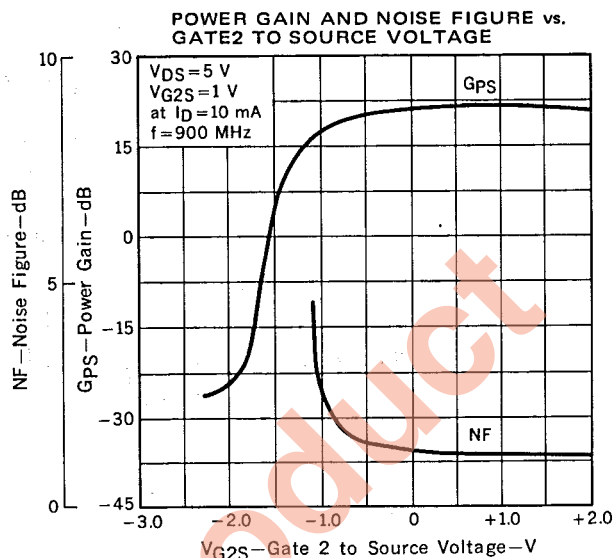
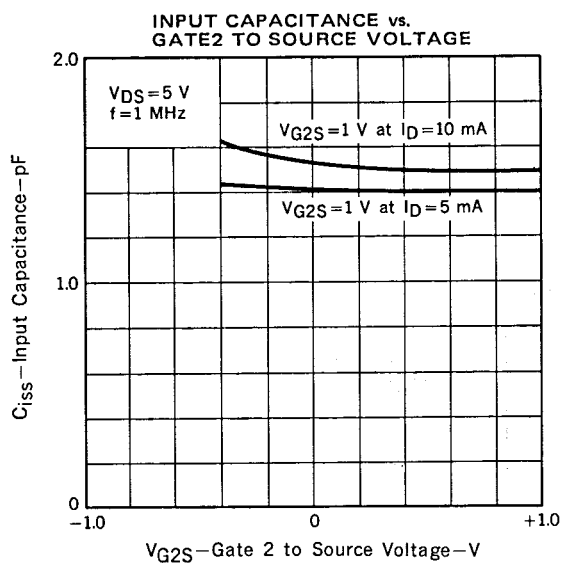
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source Breakdown Voltage	BV_{DSX}	10			V	$V_{G1S} = -4\text{ V}, V_{G2S} = 0, I_D = 20\text{ }\mu\text{A}$
Drain Current	I_{DSS}	10		80	mA	$V_{DS} = 5\text{ V}, V_{G1S} = 0, V_{G2S} = 0$
Gate1 to Source Cutoff Voltage	$V_{G1S(off)}$			-3.5	V	$V_{DS} = 5\text{ V}, V_{G2S} = 0, I_D = 100\text{ }\mu\text{A}$
Gate2 to Source Cutoff Voltage	$V_{G2S(off)}$			-3.5	V	$V_{DS} = 5\text{ V}, V_{G1S} = 0, I_D = 100\text{ }\mu\text{A}$
Gate1 Reverse Current	I_{G1SS}			10	μA	$V_{DS} = 0, V_{G1S} = -4\text{ V}, V_{G2S} = 0$
Gate2 Reverse Current	I_{G2SS}			10	μA	$V_{DS} = 0, V_{G2S} = -4\text{ V}, V_{G1S} = 0$
Forward Transfer Admittance	$ Y_{fs} $	25	35		mS	$V_{DS} = 5\text{ V}, V_{G2S} = 1\text{ V}, I_D = 10\text{ mA}$ $f = 1.0\text{ kHz}$
Input Capacitance	C_{iss}	1.0	1.5	2.0	pF	$V_{DS} = 5\text{ V}, V_{G2S} = 1\text{ V}, I_D = 10\text{ mA}$ $f = 1.0\text{ MHz}$
Reverse Transfer Capacitance	C_{rss}		0.02	0.035	pF	
Power Gain	G_{PS}	16.0	20.0		dB	$V_{DS} = 5\text{ V}, V_{G2S} = 1\text{ V}, I_D = 10\text{ mA}$ $f = 900\text{ MHz}$
Noise Figure	NF		1.1	2.5	dB	

 I_{DSS} Classification (Unit: mA)

Marking	N	M	L	K
I_{DSS}	10 to 25	20 to 35	30 to 50	45 to 80

Precaution: Avoid high static voltages or electric fields so that this device would not suffer from any damage due to those voltages or fields.

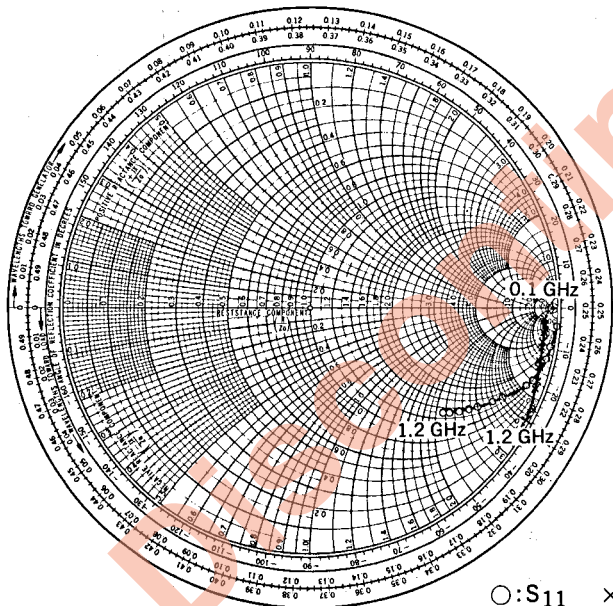
TYPICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)



S-PARAMETER ($V_{DS} = 5\text{ V}$, $V_{G2S} = 1\text{ V}$, $I_D = 10\text{ mA}$)

FREQUENCY (MHz)	S11		S21		S12		S22	
	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
100.00	1.006	-4.1	4.218	174.4	0.004	97.3	0.957	-2.2
200.00	0.979	-9.6	4.253	162.2	0.002	11.1	0.944	-4.7
300.00	0.973	-12.4	4.065	156.2	0.005	82.6	0.954	-6.0
400.00	0.939	-18.2	3.944	148.2	0.007	92.1	0.952	-9.4
500.00	0.902	-19.7	3.854	145.7	0.005	79.3	0.944	-9.9
600.00	0.879	-26.0	4.068	135.0	0.007	68.6	0.963	-14.4
700.00	0.815	-26.8	3.734	127.2	0.006	95.3	0.938	-14.1
800.00	0.805	-31.9	3.727	120.3	0.009	92.8	0.958	-18.3
900.00	0.747	-33.0	3.586	115.4	0.007	109.8	0.939	-19.5
1000.00	0.743	-36.3	3.770	108.8	0.009	140.9	0.988	-23.4
1100.00	0.678	-36.7	3.574	99.4	0.007	172.7	0.955	-24.9
1200.00	0.703	-37.4	3.573	93.9	0.012	-147.6	1.040	-27.8

10-GHz GaAs MESFET amplifier circuit diagram. The circuit includes an input matching network L_1 , a gate bias network with $V_{G2S}(1\text{ V})$ and $V_{G1S}(5\text{ V})$ sources, a drain bias network with $V_{DD}(5\text{ V})$, and an output matching network L_2 . Components include 1000 pF capacitors, $47\text{ k}\Omega$ resistors, and an RFC (radio frequency choke). The input and output are $50\text{ }\Omega$ ports. A note specifies $L_1, L_2, 35 \times 5 \times 0.2\text{ mm}$.

 $V_{DS} = 5 \text{ V}, V_{G2S} = 1 \text{ V}, I_D = 10 \text{ mA}$ 
$$\text{O}:\text{S}_{11} \quad \times:\text{S}_{22}$$
