

SI LABS GENERIC DONGLE BOARD  
WITH POWER SUPPLIES

BY

RYAN RODERICK

INTERSIL  
CORPORATION

DRAWING TITLE

Si Labs USB Dongle

SIZE  
**C**

REV. **A**

DATE:  
Wed Jan 16 11:51:02 2013

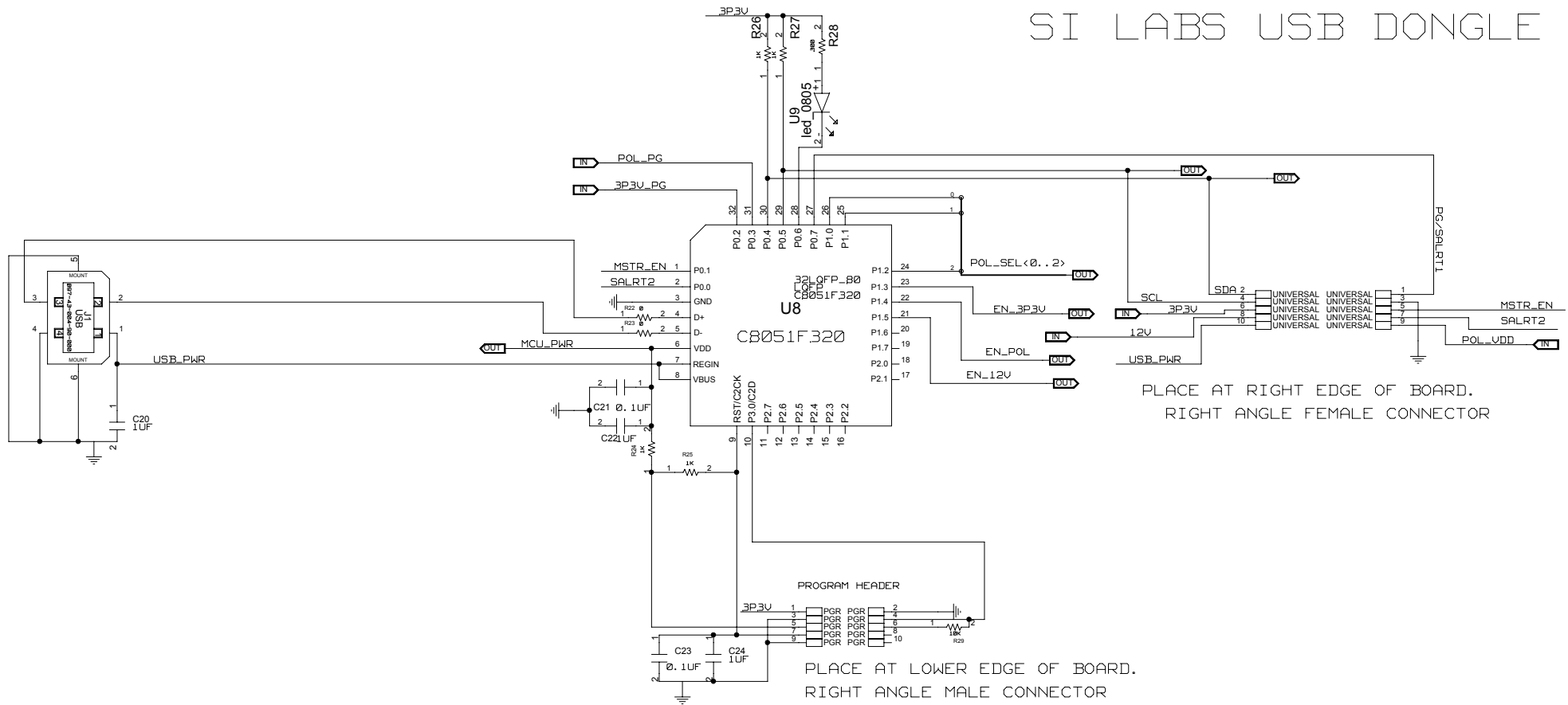
ENGINEER

Ryan Roderick  
<ENGINEER2>

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## SI LABS USB DONGLE



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UNCONTROLLER

DRAWING TITLE
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Si Labs USB Dongle

SIZE  
**C**

REV.	
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DATE.  
Mon Feb 25 09:52:04 2013

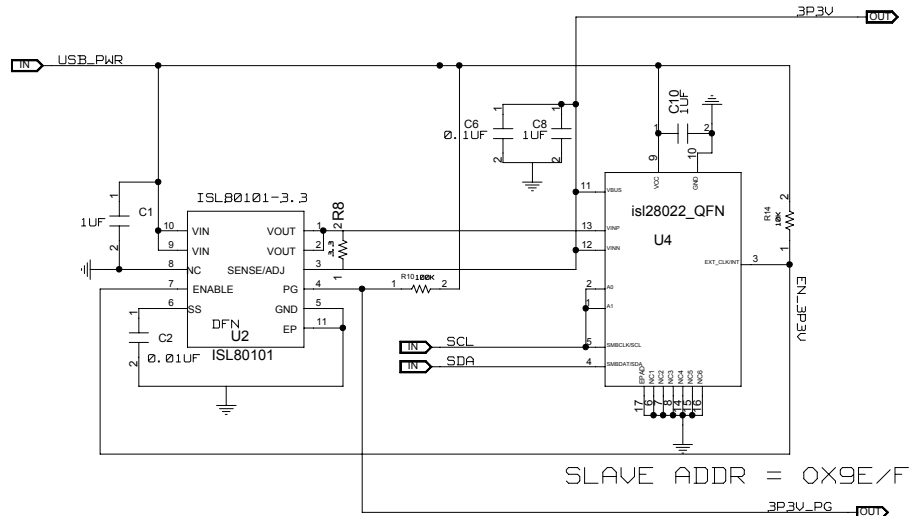
ENGINEER

Ryan Roderick  
<ENGINEER2>

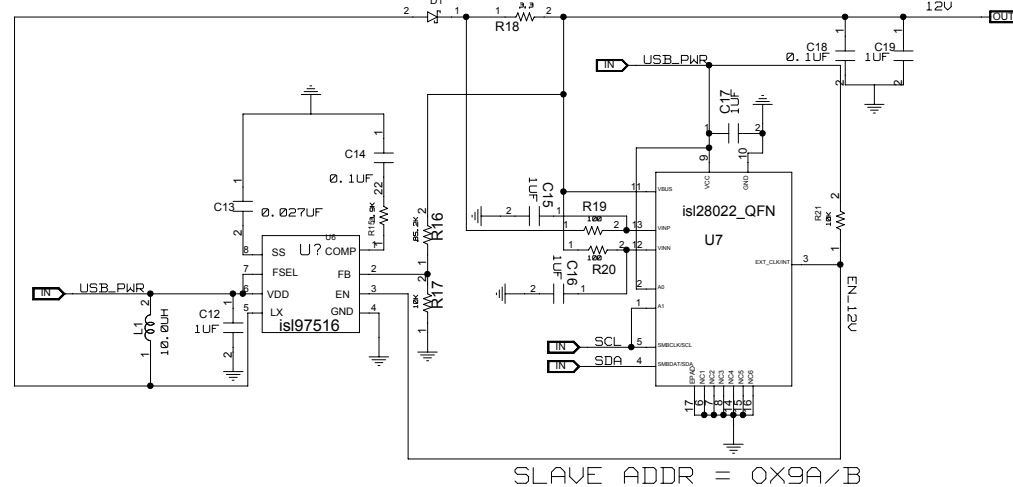
PAGE 2 OF 3



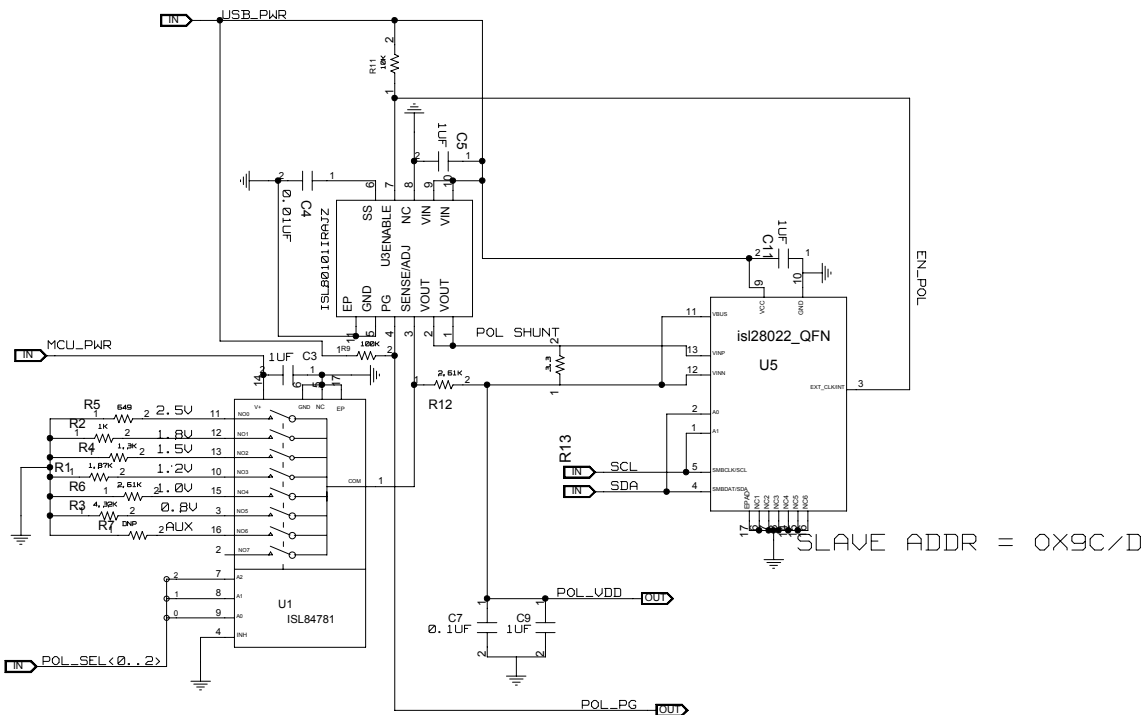
5V TO 3.3V REG 3.3V CURRENT LIMIT IS 100MA



5V TO 12V REG 12V CURRENT LIMIT IS 100MA



5V TO POL REG



POL\_VDD CURRENT LIMIT IS 100MA

INTERSIL  
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GENERATE OTHER VOLTAGES FROM  
THE USB POWER.

DRAWING TITLE

Si Labs USB Dongle

SIZE  
C

REV. A

DATE:  
Wed Jan 16 11:51:29 2013

ENGINEER Ryan Roderick  
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