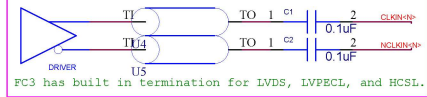


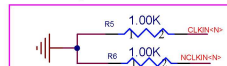
FC3 RC323xx Customer Reference Schematic

REFERENCE INPUT

100 ohm differential trace
or two 50 ohm single ended

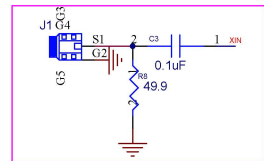


CLKIN0/CLKIN1

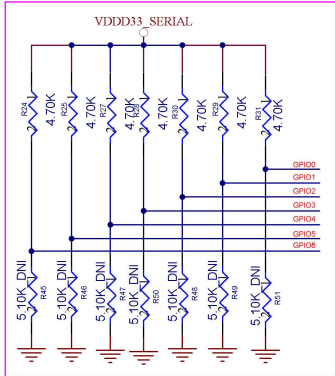


Connect with 1k pull down resistors if no Clk input or GPIO function.

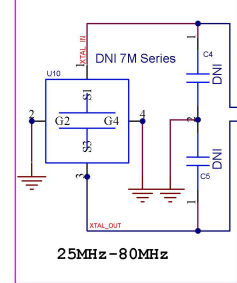
XIN Overdrive: AC-Coupled



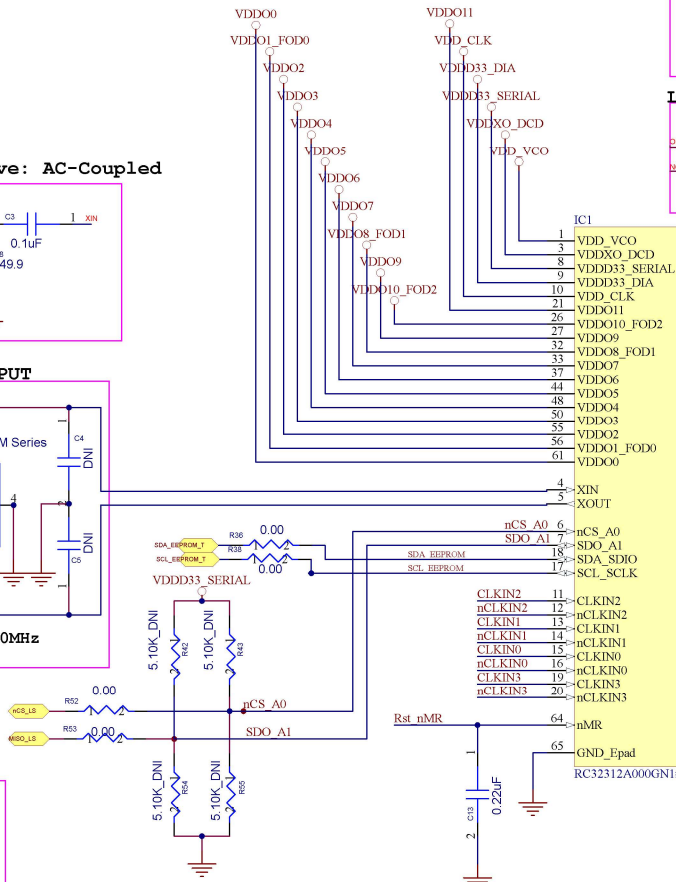
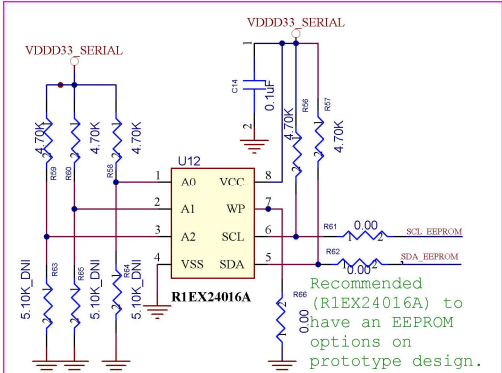
GPIO SELECTION



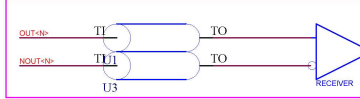
CRYSTAL INPUT



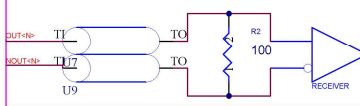
EEPROM



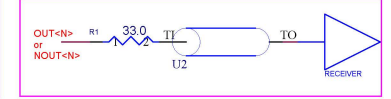
HCSSL - Internal Termination



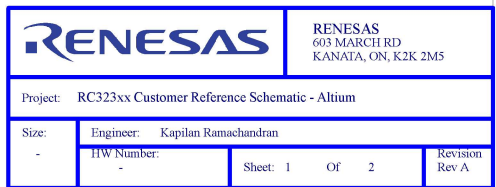
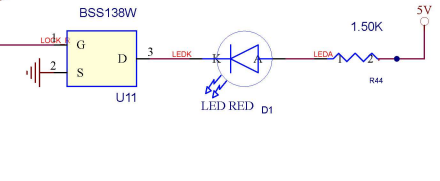
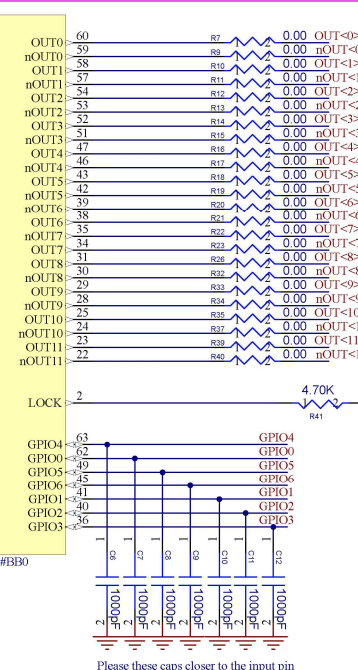
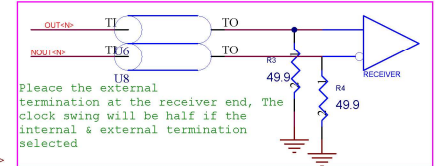
LVDS



LVC MOS



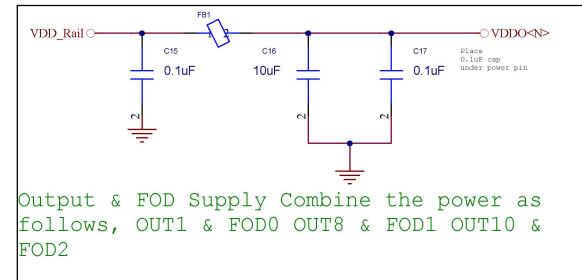
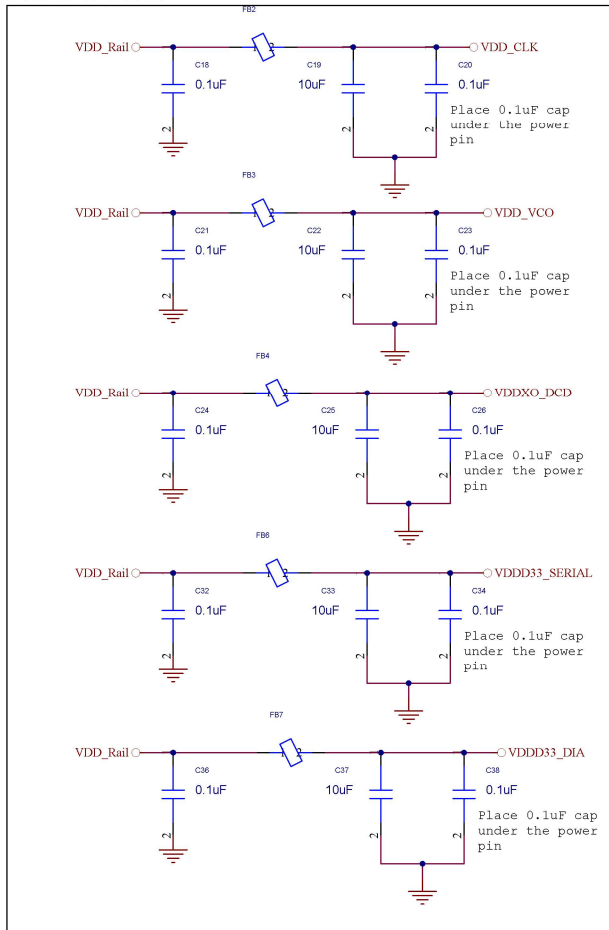
HCSSL - External Termination



FC3 RC323xx Customer Reference Schematic

RC32312A, R22312A Clock Gen & Jitter Attenuation

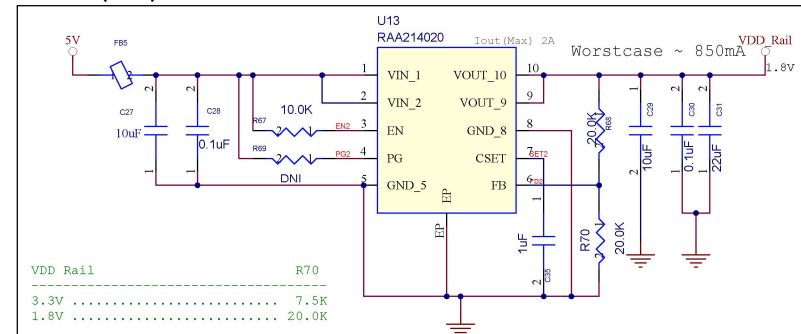
POWER FILTER



Output & FOD Supply Combine the power as follows, OUT1 & FOD0 OUT8 & FOD1 OUT10 & FOD2

VDDD33_DIA & VDDD33_SERIAL supports 1.8 & 3.3V however, VDDD33_DIA to 1.8V for lower power consumptions, VDDD33_SERIAL Selects the I2C comm voltage, 3.3V may required for EEPROM loading

POWER (LDO)



		RENESAS 603 MARCH RD KANATA, ON, K2K 2M5	
Project: RC323xx Customer Reference Schematic - Altium			
Size:	Engineer: Kapilan Ramachandran		
-	HW Number:	Sheet: 2 Of 2	Revision Rev A
	-		