Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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P1 98.2



NNEL MOS FIELD EFFECT POWER TRANSISTORS

2SK1060,2SK1060-Z

DESCRIPTION

The 2SK1060, 2SK1060-Z are N-Channel MOS Field Effect Power Transistor designed for solenoid, motor and lamp driver.

FEATURES

- 4 V Gate Drive Logic level -
- Low R_{DS(on)}
- No Second Breakdown
- **Designed for Hybrid Integrated Circuits**

ABSOLUTE MAXIMUM RATINGS

Storage Temperature -55 to +150 °C Junction Temperature 150 °C Maximum

Maximum Power Dissipations

W Total Power Dissipation*..... Total Power Dissipation (T_c ** = 25 °C). . W Maximum Voltages and Currents (T_a = 25 °C) Drain to Source Voltage 100 ٧ V_{DSS} ±20 Gate to Source Voltage V_{GSS} Drain Current (DC) ±5 Α ID(DC) ID(pulse) Drain Current (pulse)***....

ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
R _{DS(on)}	Drain to Source On-State Resistance		0.18	0.27	Ω	V _{GS} = 10 V, I _D = 3 A
RDS(on)	Drain to Source On-State Resistance		0.22	0.38	Ω	V _{GS} = 4 V, I _D = 3 A
VGS(off)	Gate to Source Cutoff Voltage	1.0		2.5	V	V_{DS} = 10 V, I_D = 1 mA
lyfs	Forward Transfer Admittance	4.0			s	$V_{DS} = 10 \text{ V}, I_{D} = 3 \text{ A}$
IDSS	Drain Leakage Current			10	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
IGSS	Gate to Source Leakage Current			±100	nA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Ciss	Input Capacitance		900		рF	V _{DS} = 10 V V _{GS} = 0 f = 1 MHz
Coss	Output Capacitance		250		pF	
C _{rss}	Reverse Transfer Capacitance		50		pF	
^t d(on)	Turn-On Delay Time		10		ns	
t _r	Rise Time		40		ns	$I_D = 3 A, V_{DD} = 50 V$ $R_L = 17 \Omega$ $R_{in} = 10 \Omega$
^t d(off)	Turn-Off Delay Time		110		ns	
tf	Fall Time		30		ns	

^{*} Mounted on ceramic substrate of 7.5 cm 2 x 0.7 mm

^{**} T_c = 25 °C

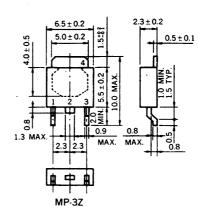
^{***} PW \leq 10 μ s, Duty Cycle \leq 1 %

Sections 25 (and one

PACKAGE DIMENSIONS (Unit: mm)

2SK1060 6.5 ± 0.2 1.5:83 5.0 ± 0.2 0.5 ± 0.1 1.6 ± 0.2 1.3 MAX 0.6 ± 0.1 0.6 ± 0.1

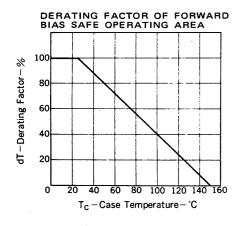
2SK1060-Z

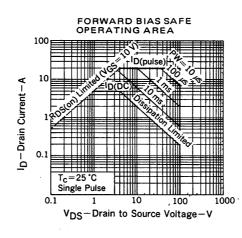


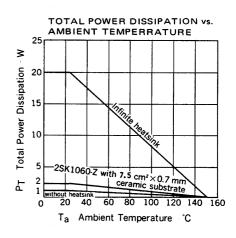
- 1. Gate 2. Drain
- 3. Source
- 4. Drain (Fin)

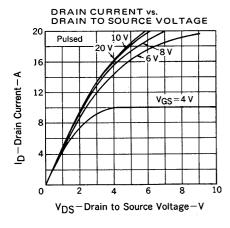
TYPICAL CHARACTERISTICS (Ta = 25 °C)

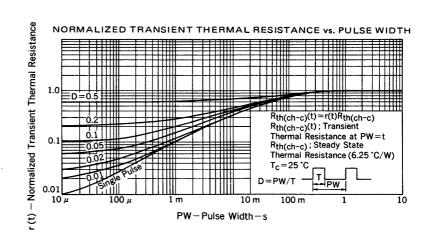
MP-3

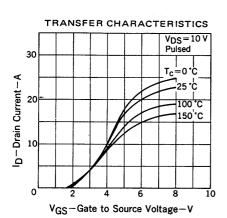




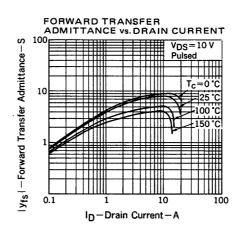


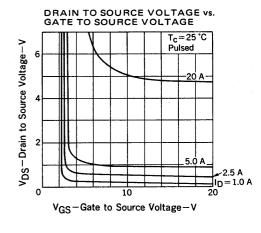


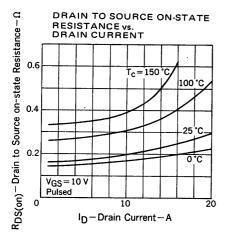


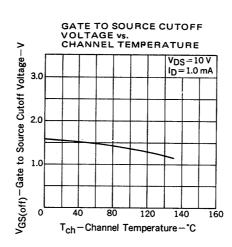


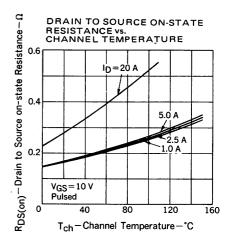
PRE ARMORA

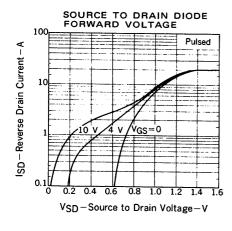


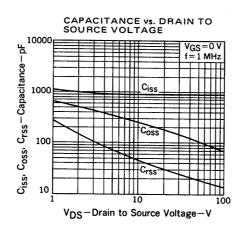


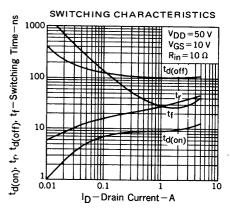


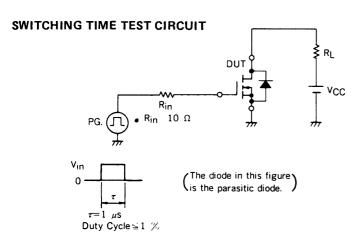


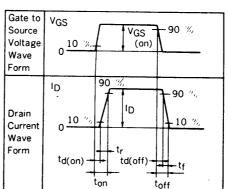












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GATE CHARGE TEST CIRCUIT

