Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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JUNCTION FIELD EFFECT TRANSISTOR 2SK3230B

N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR FOR IMPEDANCE CONVERTER OF ECM

DESCRIPTION

The 2SK3230B is suitable for converter of ECM.

General-purpose product.

FEATURES

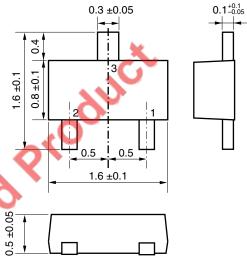
- · Low noise:
 - -108.5 dB TYP. (V_{DD} = 2.0 V, C = 5 pF, R_L = 2.2 k Ω)
- Especially suitable for audio and telephone
- · Small package:

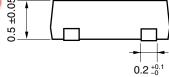
SC-89 (TUSM)

ORDERING INFORMATION

PART NUMBER	PACKAGE	
2SK3230B	SC-89 (TUSM)	

PACKAGE DRAWING (Unit: mm)

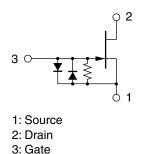




ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V _{GS} = -1.0 V)	Vosx	20	V
Gate to Drain Voltage	Vgdo	-20	V
Drain Current	lσ	10	mA
Gate Current	lg	10	mA
Total Power Dissipation	Рт	100	mW
Junction Temperature	T_j	125	°C
Storage Temperature	Tstg	-55 to +125	°C

EQUIVALENT CIRCUIT



Caution Please take care of ESD (Electro Static Discharge) when you handle the device in this document.

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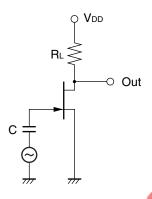
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Cut-off Current	Ipss	V _{DS} = 2.0 V, V _{GS} = 0 V	90	200	430	μΑ
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = 2.0 \text{ V}, I_{D} = 1.0 \ \mu\text{A}$		-0.37	-1.0	V
Forward Transfer Admittance	Yfs1	V _{DS} = 2.0 V, I _D = 30 μA, f = 1.0 kHz	300	480		μS
	yfs2	V _{DS} = 2.0 V, V _{GS} = 0 V, f = 1.0 kHz	750	1300		μS
Input Capacitance	Ciss	V _{DS} = 2.0 V, V _{GS} = 0 V, f = 1.0 MHz		4.0		pF
Voltage Gain	Gv	V_{DD} = 2.0 V, C = 5 pF, R _L = 2.2 k Ω ,		-1.0		dB
		V _{IN} = 10 mV, f = 1 kHz				
Noise Voltage	NV	V_{DD} = 2.0 V, C = 5 pF, R _L = 2.2 k Ω ,		-108.5		dB
		A-curve				

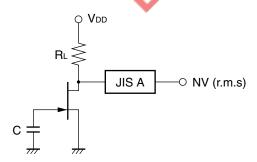
IDSS CLASSIFICATION

MARKING	CE	CF	СН	Cl			
Ioss (μA)	90 to 180	150 to 240	210 to 350	320 to 430			
VOLTAGE GAI	N TEST CIRCU	ІТ	8				
○ VDD RL <			روح				
1	Out	O					
$c = \frac{1}{1}$							
Θ		D .					

VOLTAGE GAIN TEST CIRCUIT



NOISE VOLTAGE TEST CIRCUIT

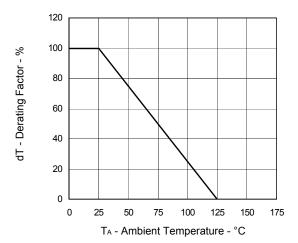


les - Gate to Source Current - µA

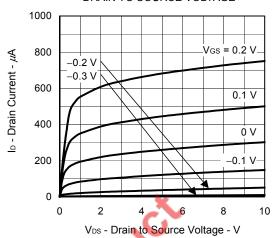
Ciss - Input Capacitance - pF

TYPICAL CHARACTERISTICS (TA = 25°C)

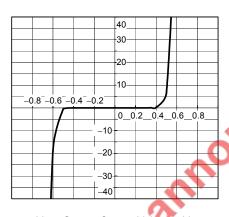
DERATING FACTOR OF POWER DISSIPATION



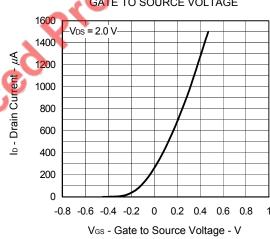
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



GATE TO SOURCE CURRENT vs. GATE TO SOURCE VOLTAGE

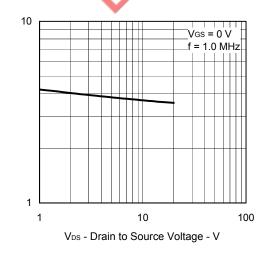


DRAIN CURRENT vs.
GATE TO SOURCE VOLTAGE

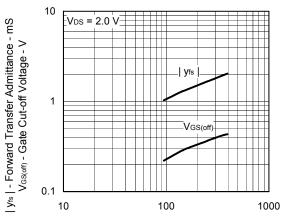


V_{GS} - Gate to Source Voltage - V

INPUT CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

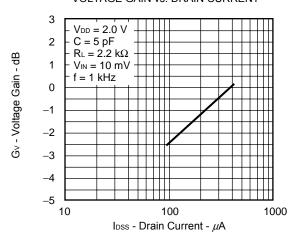


FORWARD TRANSFER ADMITTANCE AND GATE CUT-OFF VOLTAGE vs. ZERO GATE VOLTAGE DRAIN CURRENT

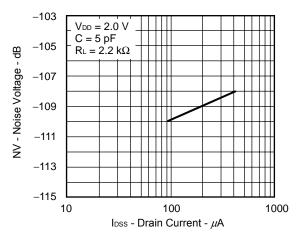


IDSS - Zero Gate Voltage Drain Current - μA

VOLTAGE GAIN vs. DRAIN CURRENT



NOISE VOLTAGE vs. DRAIN CURRENT



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