

**PS9031** 

2.5 A OUTPUT CURRENT, HIGH CMR, IGBT GATE DRIVE, 5-PIN SOP (LSO5 WITH 8mm CREEPAGE DISTANCE) PHOTOCOUPLER

R08DS0131EJ0200 Rev.2.00 Mar 11, 2016

#### **DESCRIPTION**

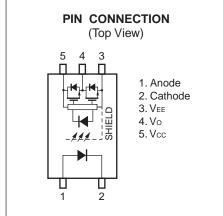
The PS9031 is an optically coupled isolator containing a GaAlAs LED on the input side and a photodiode, a signal processing circuit and power MOSFETs on the output side on one chip.

#### **FEATURES**

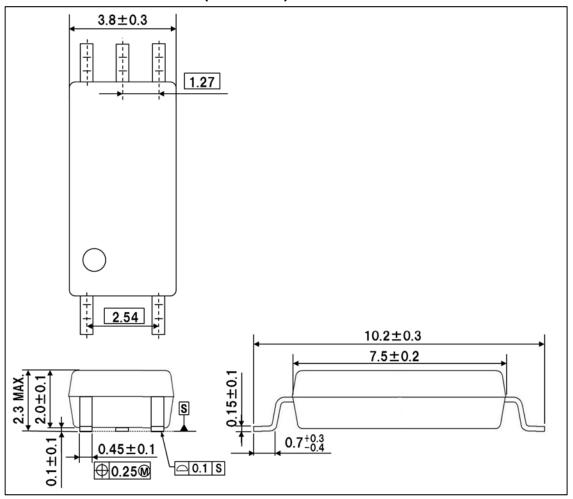
- Long creepage distance (8 mm MIN.)
- Large peak output current (2.5 A MAX., 2.0 A MIN.)
- High speed switching ( $t_{PLH}$ ,  $t_{PHL} = 175$  ns MAX.)
- UVLO (Under Voltage Lock Out) protection with hysteresis
- High common mode transient immunity (CM<sub>H</sub>, CM<sub>L</sub> =  $\pm 50 \text{ kV/}\mu\text{s}$  MIN.)
- Operating Ambient Temperature (125 °C MAX.)
- Embossed tape product: PS9031-F3 : 3000 pcs/reel
- Pb-Free product
- · Safety standards
  - UL approved: UL1577, Double protection
  - CSA approved: CA5A, CAN/CSA-C22.2 No.60065, CAN/CSA-C22.2 No.60950-1, Reinforced insulation
  - VDE approved: DIN EN 60747-5-5 (Option)



- IGBT, Power MOS FET Gate Driver
- Industrial inverter
- AC Servo



## PACKAGE DIMENSIONS (UNIT: mm)

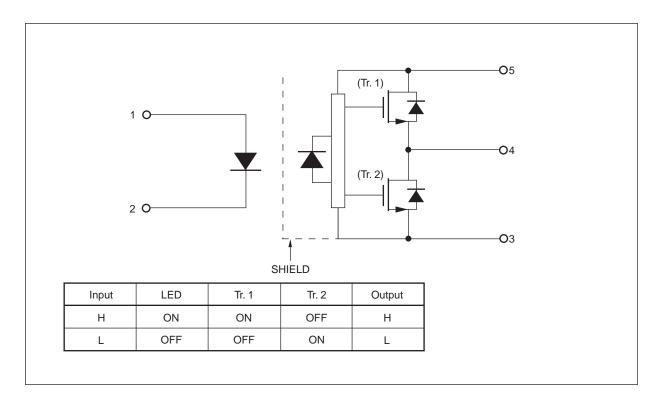


Weight: 0.119g (typ.)

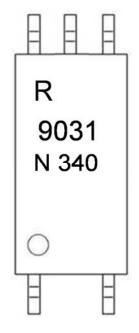
## PHOTOCOUPLER CONSTRUCTION

Parameter	MIN.
Air Distance	8.0 mm
Outer Creepage Distance	8.0 mm
Isolation Distance	0.15 mm

## **BLOCK DIAGRAM**



## MARKING EXAMPLE



R		An initial of "Renesas"			
9031		Product Part Number			
0		No.1	No.1 pin Mark, Anode Mark		
N340	N	Rank	Rank Code		
	340	Asse	Assembly Lot		
		3	Last one-digit of Assembly Year		
		40 Weekly Serial Code			

#### ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS9031	PS9031-Y-AX	Pb-Free and	20 pcs (Tape 20 pcs cut)	Standard products	PS9031
PS9031-F3	PS9031-Y-F3-AX	Halogen Free (Ni/Pd/Au)	Embossed Tape 3 000 pcs/reel	(UL,CSA approved)	
PS9031-V	PS9031-Y-V-AX		20 pcs (Tape 20 pcs cut)	UL,CSA approved	
PS9031-V-F3	PS9031-Y-V-F3-AX		Embossed Tape 3 000 pcs/reel	DIN EN 60747-5-5 (VDE 0884-5): 2011-11 approved (Option)	

Note: \*1. For the application of the Safety Standard, following part number should be used.

## ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current	lF	25	mA
	Peak Transient Forward Current (Pulse Width < 1 µs)	I <sub>F</sub> (TRAN)	1.0	Α
	Reverse Voltage	V <sub>R</sub>	5	V
	Power Dissipation *1	PD	45	mW
Detector	High Level Peak Output Current *2	IOH (PEAK)	2.5	Α
	Low Level Peak Output Current *2	I <sub>OL</sub> (PEAK)	2.5	Α
	Supply Voltage	(Vcc - Vee)	0 to 35	V
	Output Voltage	Vo	0 to Vcc	V
	Power Dissipation *3	Pc	250	mW
Isolation Vo	oltage *4	BV	5 000	Vr.m.s.
Operating	Frequency	f	200	kHz
Operating A	Operating Ambient Temperature		-40 to +125	°C
Storage Te	emperature	T <sub>stg</sub>	−55 to +150	°C

Notes: \*1. Reduced to 1.2 mW/°C at T<sub>A</sub> = 110°C or more.

### RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	(Vcc - Vee)	15		30	V
Forward Current (ON)	I <sub>F</sub> (ON)	8	10	12	mA
Forward Voltage (OFF)	V <sub>F</sub> (OFF)	-2		0.8	V
Operating Ambient Temperature	TA	-40		125	°C

<sup>\*2.</sup> Maximum pulse width = 10  $\mu$ s, Maximum duty cycle = 0.2%

<sup>\*3.</sup> Reduced to 3.9 mW/ $^{\circ}$ C at T<sub>A</sub> = 90 $^{\circ}$ C or more.

<sup>\*4.</sup> AC voltage for 1 minute at  $T_A$  = 25°C, RH = 60% between input and output. Pins 1-2 shorted together, 3-5 shorted together.

#### **ELECTRICAL CHARACTERISTICS**

## (at RECOMMENDED OPERATING CONDITIONS, VEE=GND, unless otherwise Specified)

	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	I <sub>F</sub> = 10 mA, T <sub>A</sub> = 25°C	1.35	1.56	1.75	V
	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 3 V, T <sub>A</sub> = 25°C			10	μΑ
	Input Capacitance	Cin	f = 1 MHz, V <sub>F</sub> = 0 V		30		pF
Detector	High Level Output Current	Іон	$V_0 = (V_{CC} - 4 V)^{*2}$	0.5	2.2		Α
			$V_0 = (V_{CC} - 15 \text{ V})^{*3}$	2.0			
	Low Level Output Current	loL	$V_O = (V_{EE} + 2.5 \text{ V})^{*2}$	0.5	2.4		Α
			$V_O = (V_{EE} + 15 \text{ V})^{*3}$	2.0			
	High Level Output Voltage	Vон	I <sub>O</sub> = -100 mA *4	Vcc - 3.0	Vcc - 1.3		V
	Low Level Output Voltage	Vol	I <sub>O</sub> = 100 mA		0.2	0.5	V
	High Level Supply Current	Іссн	V <sub>O</sub> = Open, I <sub>F</sub> = 10 mA		1.7	2.2	mA
	Low Level Supply Current	Iccl	$V_O$ = Open, $V_F$ = 0 to 0.8V		1.7	2.2	mA
	UVLO Threshold	V <sub>UVLO+</sub>	V <sub>O</sub> > 5 V, I <sub>F</sub> = 10 mA	10.8	12.3	13.4	V
		V <sub>UVLO-</sub>		9.5	11.0	12.5	
	UVLO Hysteresis	UVLO <sub>HYS</sub>	V <sub>O</sub> > 5 V, I <sub>F</sub> = 10 mA	0.4	1.3		V
Coupled	Threshold Input Current	I <sub>FLH</sub>	I <sub>O</sub> = 0 mA, V <sub>O</sub> > 5 V		1.7	4.0	mA
	$(L \rightarrow H)$						
	Threshold Input Voltage $(H \rightarrow L)$	V <sub>FHL</sub>	I <sub>O</sub> = 0 mA, V <sub>O</sub> < 5 V	0.8			<b>V</b>

Notes: \*1. Typical values at  $T_A = 25$ °C,  $V_{CC} - V_{EE} = 30$  V.

#### **SWITCHING CHARACTERISTICS**

### (at RECOMMENDED OPERATING CONDITIONS, VEE=GND, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Propagation Delay Time (L $\rightarrow$ H)	tplH	$R_g = 10 \Omega, C_g = 10 nF,$		80	175	ns
Propagation Delay Time (H $\rightarrow$ L)	<b>t</b> PHL	f = 10 kHz,		105	175	ns
Pulse Width Distortion (PWD)	tphl-tplh	Duty Cycle = 50%,		25	75	ns
Propagation Delay Time	t <sub>РНL</sub> —t <sub>РLН</sub>	I <sub>F</sub> = 10 mA	-90		90	ns
(Difference Between Any Two Products)						
Rise Time	tr			40		ns
Fall Time	tf			40		ns
Common Mode Transient Immunity at High Level Output	[СМн]	T <sub>A</sub> = 25°C, I <sub>F</sub> = 10 mA, V <sub>CC</sub> = 30 V, V <sub>CM</sub> = 1.5 kV	50			kV/μs
Common Mode Transient Immunity at Low Level Output	CM <sub>L</sub>	$T_A = 25$ °C, $I_F = 0$ mA, $V_{CC} = 30$ V, $V_{CM} = 1.5$ kV	50			kV/μs

Notes: \*1. Typical values at  $T_A = 25$ °C,  $V_{CC}-V_{EE} = 30$  V.

<sup>\*2.</sup> Maximum pulse width = 50  $\mu$ s, Maximum duty cycle = 0.5%.

<sup>\*3.</sup> Maximum pulse width = 10  $\mu$ s, Maximum duty cycle = 0.2%.

<sup>\*4.</sup> V<sub>OH</sub> is measured with the DC load current in this testing (Maximum pulse width = 2 ms, Maximum duty cycle = 20%).

**TEST CIRCUIT** 

 $V_{\text{CC}}$ 1.0 µF Юн

Fig. 1 Іон Test Circuit

Fig. 2 IoL Test Circuit

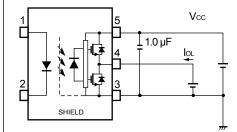


Fig. 3 Voн Test Circuit

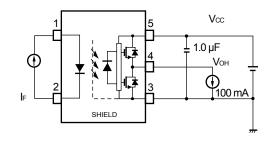


Fig. 4 Vol Test Circuit

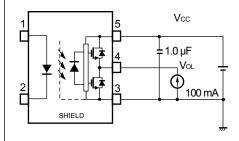


Fig. 5 Icch/IccL Test Circuit

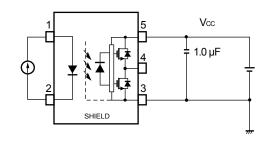
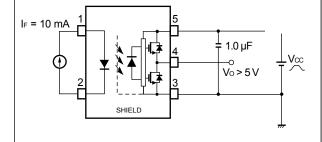


Fig. 6 UVLO Test Circuit





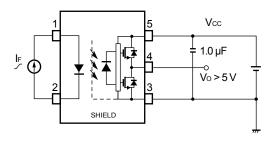
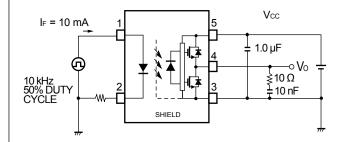


Fig. 8 tplh, tphl, tr, tf Test Circuit and Wave Forms



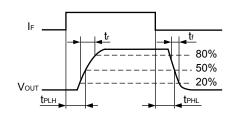
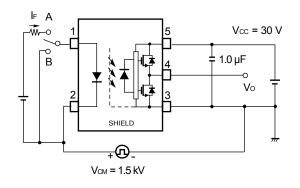
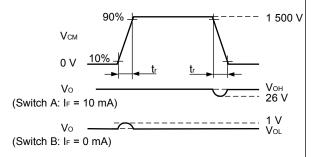
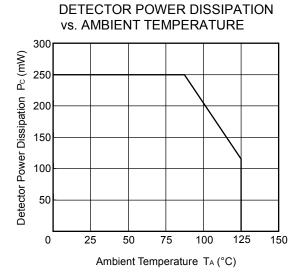


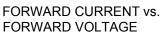
Fig. 9 CMR Test Circuit and Wave Forms

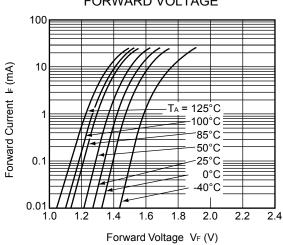




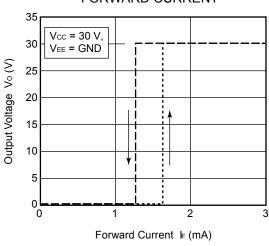
## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise specified)



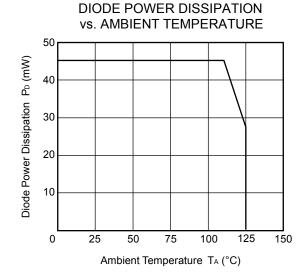




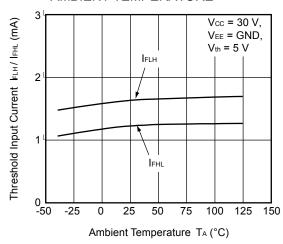
## OUTPUT VOLTAGE vs. FORWARD CURRENT



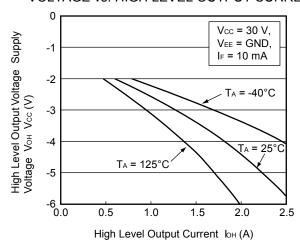
### **Remark** The graphs indicate nominal characteristics.



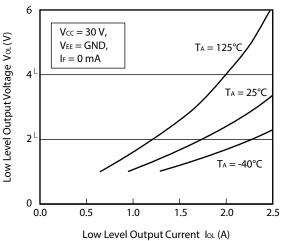
## THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



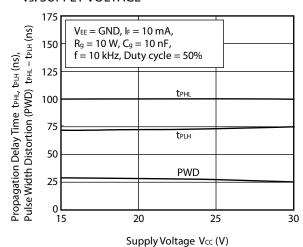
## HIGH LEVEL OUTPUT VOLTAGE SUPPLY VOLTAGE vs. HIGH LEVEL OUTPUT CURRENT



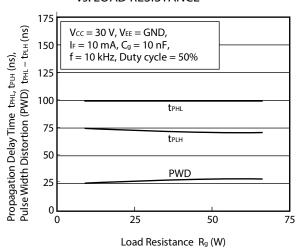
## LOW LEVEL OUTPUT VOLTAGE vs. LOW LEVEL OUTPUT CURRENT



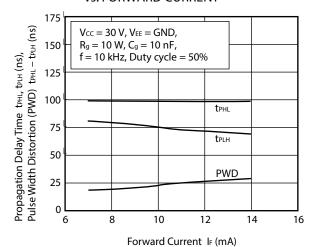
#### PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. SUPPLY VOLTAGE



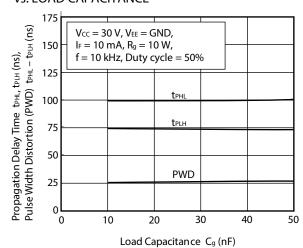
#### PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. LOAD RESISTANCE



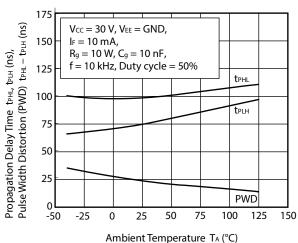
# PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. FORWARD CURRENT



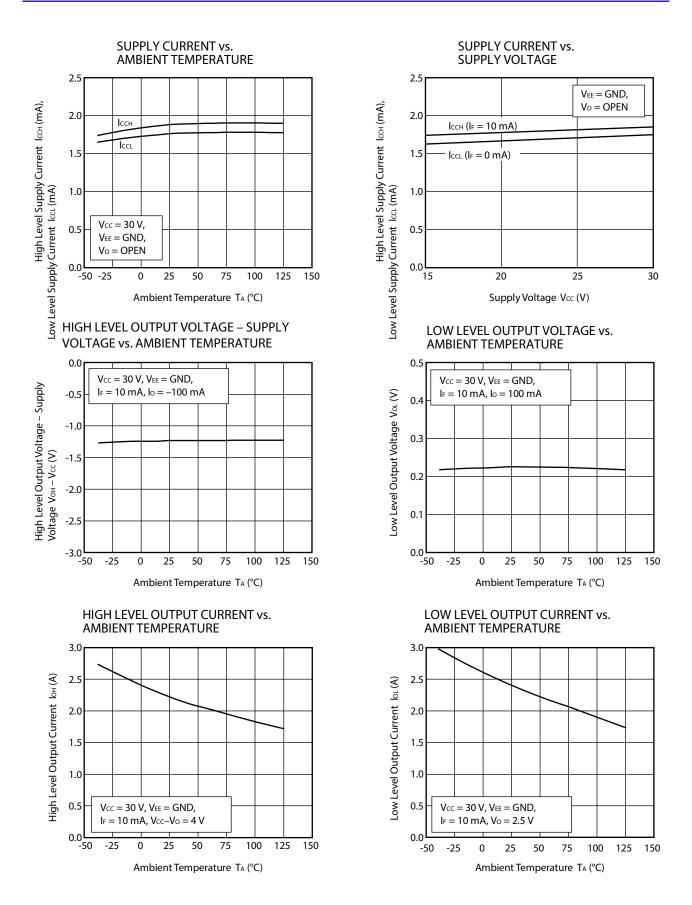
PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. LOAD CAPACITANCE



#### PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

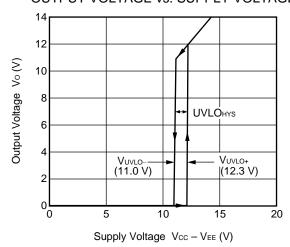


**Remark** The graphs indicate nominal characteristics.



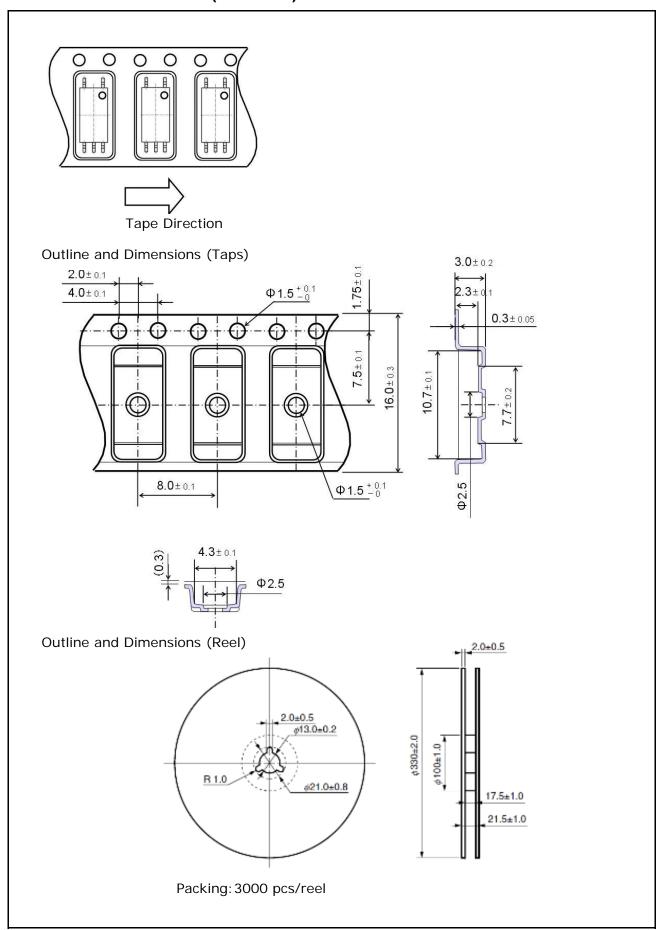
Remark The graphs indicate nominal characteristics.

### OUTPUT VOLTAGE vs. SUPPLY VOLTAGE

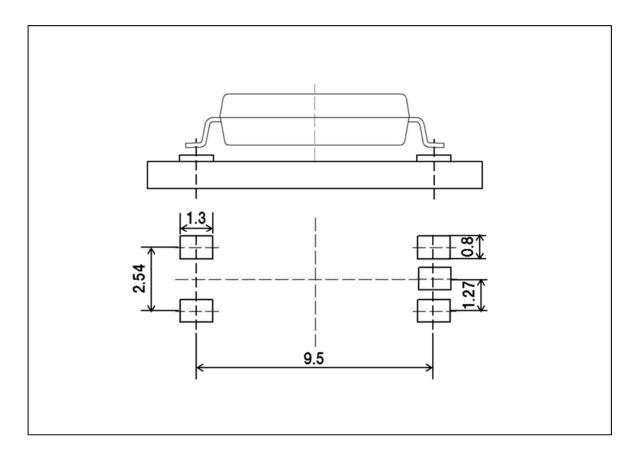


**Remark** The graphs indicate nominal characteristics.

## **TAPING SPECIFICATIONS (UNIT: mm)**



## RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



Remark All dimensions in this figure must be evaluated before use.

#### **NOTES ON HANDLING**

- 1. Recommended soldering conditions
  - (1) Infrared reflow soldering

• Peak reflow temperature 260°C or below (package surface temperature)

Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

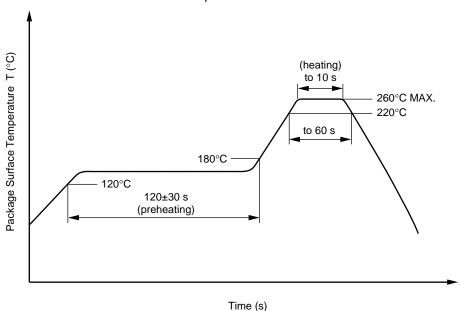
Time to preheat temperature from 120 to 180°C
 Number of reflows
 Three

• Flux Rosin flux containing small amount of chlorine (The flux

with a maximum chlorine content of 0.2 Wt% is

recommended.)

#### Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

• Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below

Time (each pins)
 3 seconds or less

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

#### (4) Cautions

• Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

#### 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

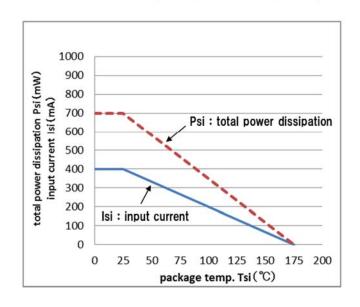
#### **USAGE CAUTIONS**

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. Board designing
  - (1) By-pass capacitor of more than 1.0  $\mu$ F is used between VCC and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
  - (2) When designing the printed wiring board, ensure that the pattern of the IGBT collectors/emitters is not too close to the input block pattern of the photocoupler.
    - If the pattern is too close to the input block and coupling occurs, a sudden fluctuation in the voltage on the IGBT output side might affect the photocoupler's LED input, leading to malfunction or degradation of characteristics.
    - (If the pattern needs to be close to the input block, to prevent the LED from lighting during the off state due to the abovementioned coupling, design the input-side circuit so that the bias of the LED is reversed, within the range of the recommended operating conditions, and be sure to thoroughly evaluate operation.)
- 3. Make sure the rise/fall time of the forward current is 0.5  $\mu$ s or less.
- **4.** In order to avoid malfunctions, make sure the rise/fall slope of the supply voltage is  $3 \text{ V}/\mu\text{s}$  or less.
- **5.** Avoid storage at a high temperature and high humidity.

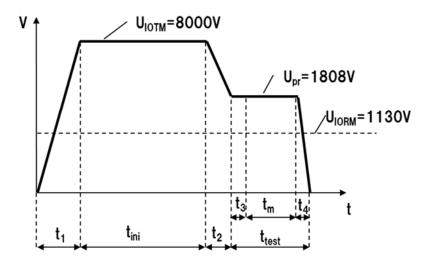
## SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/125/21	
Dielectric strength			
maximum operating isolation voltage	$U_{IORM}$	1 130	$V_{peak}$
Test voltage (partial discharge test, procedure a for type test and random test)	$U_pr$	1 808	$V_{peak}$
$U_{pr} = 1.6 \times U_{IORM.}$ , $P_d < 5 \text{ pC}$			
Test voltage (partial discharge test, procedure b for all devices) $U_{pr}$ = 1.875 × $U_{IORM.}$ , $P_d$ < 5 pC	$U_pr$	2 119	$V_{peak}$
Highest permissible overvoltage	U <sub>IOTM</sub>	8 000	$V_{peak}$
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	400	
Material group (DIN EN 60664-1 VDE0110 Part 1)		П	
Storage temperature range	T <sub>stg</sub>	-55 to +150	°C
Operating temperature range	TA	-40 to +125	°C
Isolation resistance, minimum value			
$V_{10} = 500 \text{ V dc at T}_A = 25^{\circ}\text{C}$	Ris MIN.	10 <sup>12</sup>	Ω
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> MAX. at least 100°C	Ris MIN.	10 <sup>11</sup>	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current I <sub>F</sub> , Psi = 0)	Isi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
$V_{IO}$ = 500 V dc at $T_A$ = Tsi	Ris MIN.	10 <sup>9</sup>	Ω

#### Dependence of maximum safety ratings with package temperature

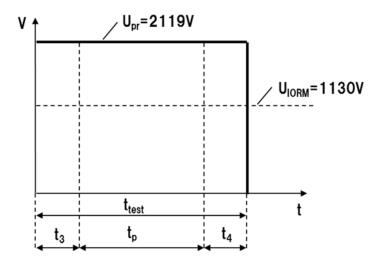


Method A Destructive Test, Type and Sample test



 $t_1,t_2$ =1 to 10 sec  $t_3,t_4$ =1 sec  $t_{m (PARTIAL \ DISCHARGE)}$ =10 sec  $t_{test}$ =12 sec  $t_{ini}$ =60 sec

#### Method b Non-destructive Test, 100% Production Test



 $t_3$ , $t_4$ =0.1 sec  $t_p$  (PARTIAL DISCHARGE) = 1.0 sec  $t_{test}$ =1.2 sec

#### Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
- 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

#### Notice

- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the
- 2. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- 3. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or
- 4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below

"Standard": Computers: office equipment: communications equipment: test and measurement equipment: audio and visual equipment: home electronic appliances: machine tools: personal electronic equipment: and industrial robots etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc.

Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by Renesas Electronics

- 6. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
- 8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
- 10. It is the responsibility of the buyer or distributor of Renesas Electronics products, who distributes, disposes of, or otherwise places the product with a third party, to notify such third party in advance of the contents and conditions set forth in this document. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics
- nt may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries
- (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.



#### SALES OFFICES

#### Renesas Electronics Corporation

http://www.renesas.com

Refer to "http://www.renesas.com/" for the latest and detailed information

California Eastern Laboratories, Inc.

4590 Patrick Henry Drive, Santa Clara, California 95054-1817, U.S.A Tel: +1-408-919-2500, Fax: +1-408-988-0279

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, German Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333 Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited ntury Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong

t 1601-1611, 16/F., Tower 2, Grand Cen : +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd.
No.777C, 100 Feet Road, HAL II Stage, Indiranagar, Bangalore, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

© 2016 Renesas Electronics Corporation. All rights reserved.