

[Upgrade to Revision]

R20TS0914EJ0100

Rev.1.00

Programming GUI for PG-FP6 Flash Memory Programmer

Jan. 16, 2023

FP6 Terminal V1.07.01

## Outline

FP6 Terminal, a programming GUI for PG-FP6 Flash Memory Programmer, has been updated from V1.07.00 to V1.07.01. For overview of PG-FP6, refer to the link below.

<https://www.renesas.com/pg-fp6>

Connect the updated FP6 Terminal to PG-FP6 and the firmware of PG-FP6 is updated.

## 1. Description

Main changes are as follows. For details, see the release note at the link below. (The note is applicable as of January 20.)

<https://www.renesas.com/software-tool/pg-fp6#document>

### 1.1 Addition of Supported Devices

#### (1) RL78 Family

Group	Part number
RL78/F23	R7F123FBG, R7F123FGG, R7F123FLG, R7F123FMG

#### (2) RH850 Family

Group	Part number
RH850/U2A6	R7F702302
RH850/U2A8	R7F702301B
RH850/U2A16	R7F702300B

### 1.2 Improved Functionality and Change

#### (1) Discontinued the Renesas Flash Programmer utility program

The product no longer includes the Renesas Flash Programmer utility program (rfp-util.exe). Use [Security Key Management Tool](#) instead.

### 1.3 Issues Fixed

#### (1) Problem of IO settings

[Applicable MCUs] RA, Renesas Synergy, RX, SuperH Family

[Description] Even though the pins from IO0 to IO5 are all set to Hi-Z on the Tool Details dialog box, some IOx pins do not become Hi-Z.

RA, Renesas Synergy, RX64x, RX65x, RX66x, RX67x, RX71x, RX72x: Set to the initial value

RX100, RX200, RX61x, RX62x, RX63x: IO2=High, IO3=Low, other=Hi-Z

This problem has been fixed in V1.07.01.

[Note] This problem still occurs when a setting file created by using the relevant version of FP6 Terminal is downloaded directly to the PG-FP6 main unit V1.07.01 or later. To avoid the problem, create a new setting file. Alternatively, open the setting file on FP6 Terminal, and then set the IO pins again on the Tool Details dialog box.

### (2) Problem of a setting file when an RPI file is selected

[Applicable MCUs] RA, Renesas Synergy, RE, RH850, RX Family

[Description] When both of the following conditions are met, Config Area and Config Setting are not programmed and/or verified.

1. In the setting file created by using FP6 Terminal, Config Area and Config Setting are excluded from the operation targets (on the Block Settings tabbed page, the items are unchecked).
  2. You have opened the file of 1 on FP6 Terminal, selected an RPI file on the Program Files tabbed page, and then clicked on [Download to FP6].  
Alternatively, you have downloaded the file of 1 to PG-FP6 by using the downset command, and then downloaded an RPI file to PG-FP6 by using the lod command.
- ※ This problem does not occur when the log shows "command-name Config Setting1(start address - end address):" while executing the command.  
E.g., Program Config Setting1(0100A100 - 0100A2FF):

This problem has been fixed in V1.07.01.

## 2. Updating the Product

FP6 Terminal can be updated free of charge. Download the installer from the link below. (The installer is available as of January 20.)

<https://www.renesas.com/software-tool/pg-fp6#download>

**Revision History**

Rev.	Date	Description	
		Page	Summary
1.00	Jan.16.23	-	First edition issued

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